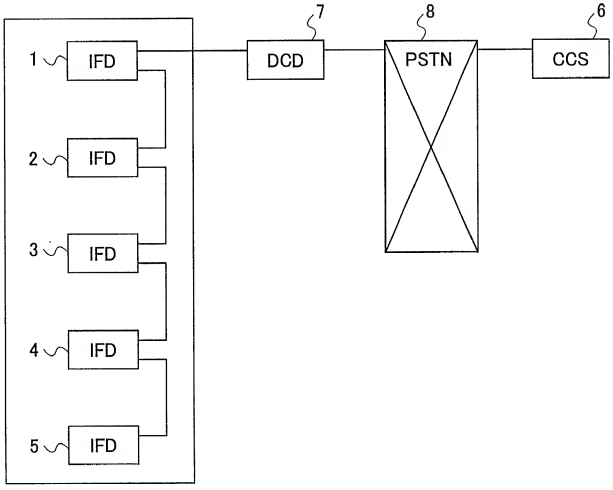
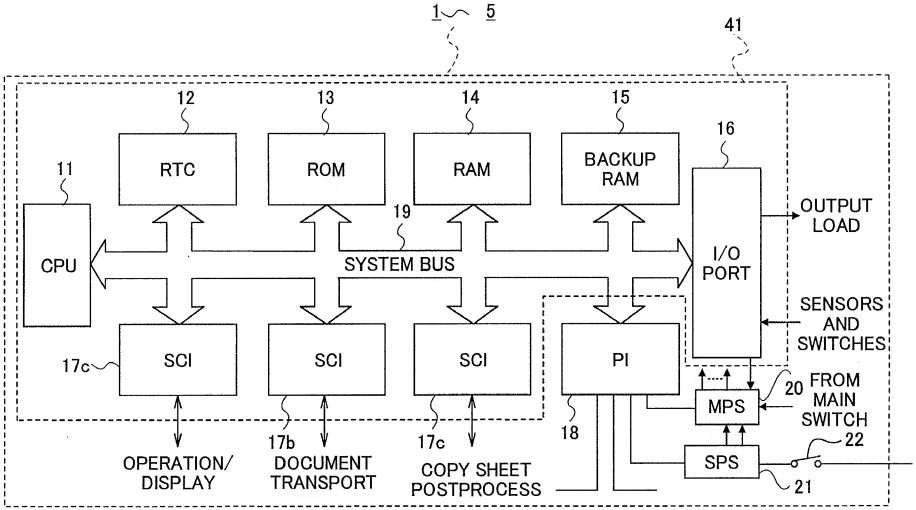
BOLON, SPIVAK, ET AL
DOCKET #: 20256US2
INV: Masaaki OGURA
SHEET 1 OF 19

FIG.1



FOCUS-2551220

FIG.2



OBLON, SPIVAK, ET AL
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INV: Masaaki OGURA
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FIG.3

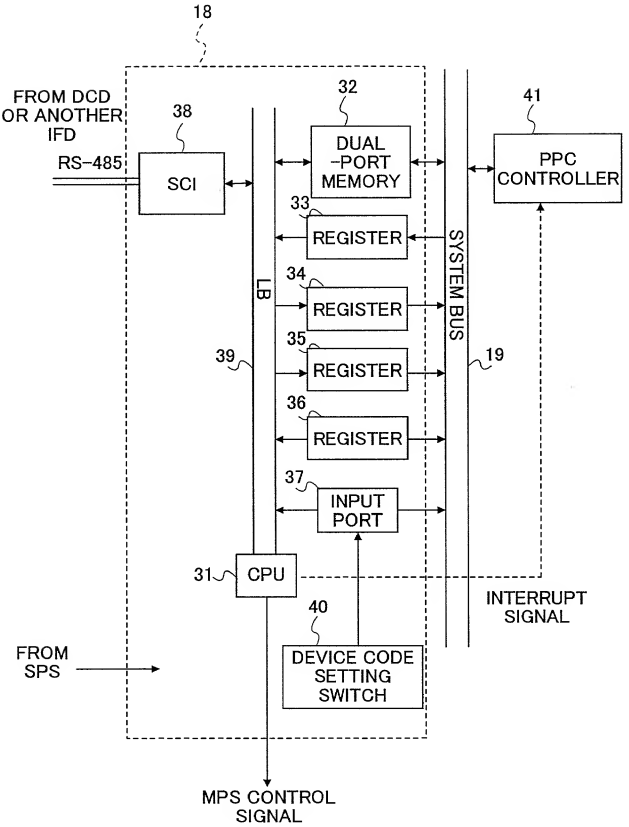
18
FROM DCD OR ANOTHER IFD
RS-485
38
SCI
32
DUAL -PORT MEMORY
41
PPC CONTROLLER
33
REGISTER
34
REGISTER
35
REGISTER
36
REGISTER
37
INPUT PORT
LB
SYSTEM BUS
39
19
31
CPU
40
DEVICE CODE SETTING SWITCH
FROM SPS
INTERRUPT SIGNAL
MPS CONTROL SIGNAL

FIG.4

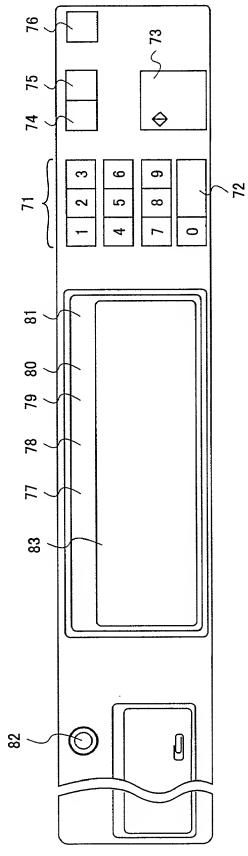

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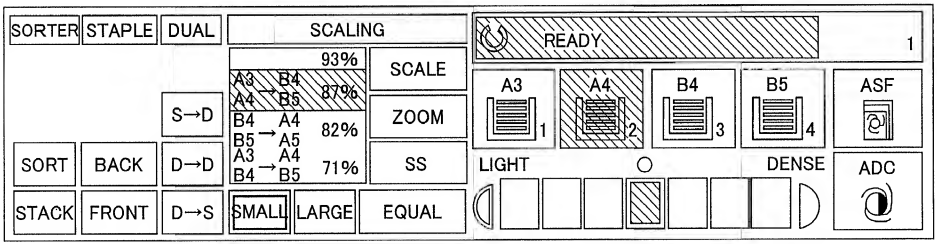
FIG.5



OBLON, SPIVAK, ET AL
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INV: Masashi OGURA
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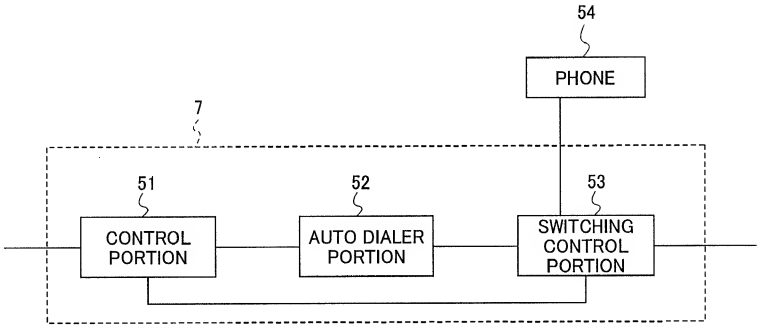
EJECT FOREIGN

FIG.6



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INV.: Masaaki OGURA
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FIG.7

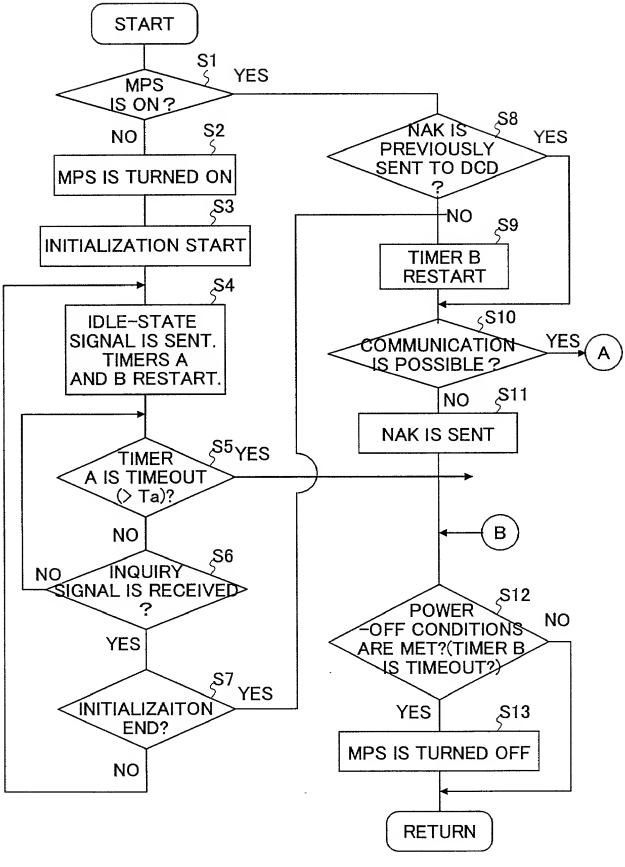
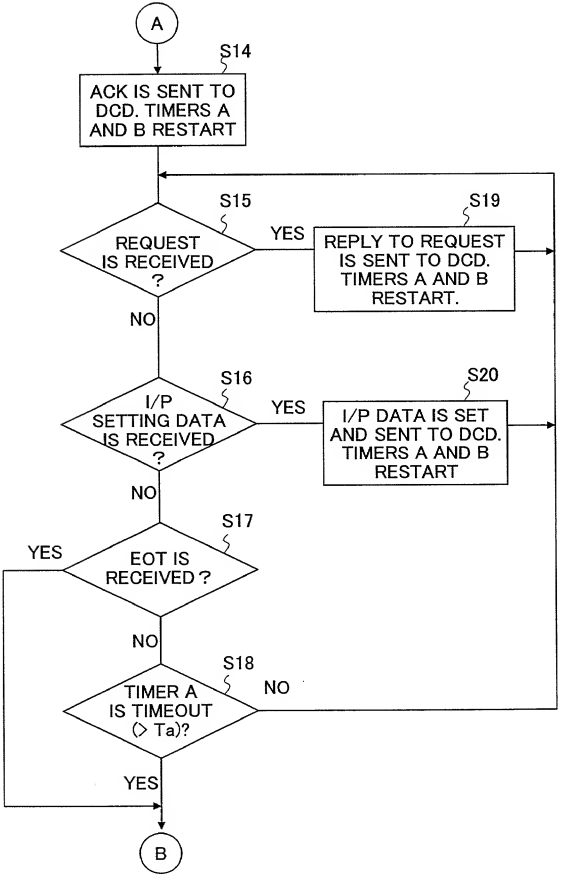
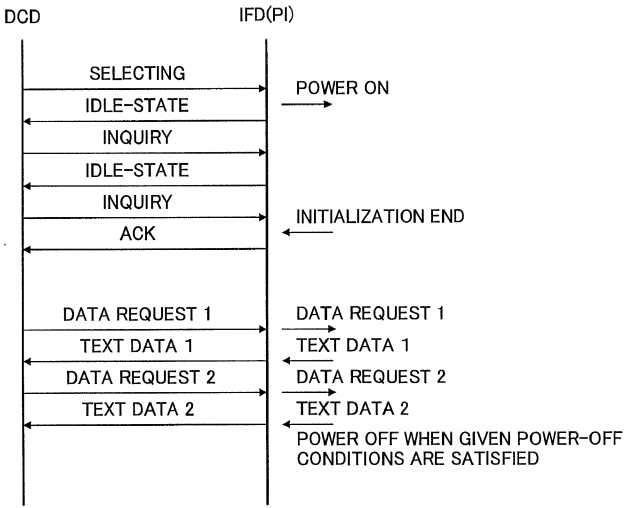

START
S1
MPS IS ON?
YES
NO
S2
MPS IS TURNED ON
S3
INITIALIZATION START
S4
IDLE-STATE SIGNAL IS SENT. TIMERS A AND B RESTART.
S5
TIMER A IS TIMEOUT (> Ta)?
YES
NO
S6
INQUIRY SIGNAL IS RECEIVED ?
NO
YES
S7
INITIALIZAITON END?
YES
NO
S8
NAK IS PREVIOUSLY SENT TO DCD ?
YES
NO
S9
TIMER B RESTART
S10
COMMUNICATION IS POSSIBLE ?
YES
A
NO
S11
NAK IS SENT
B
S12
POWER -OFF CONDITIONS ARE MET?(TIMER B IS TIMEOUT?)
NO
YES
S13
MPS IS TURNED OFF
RETURN

FIG.8


A
S14
ACK IS SENT TO DCD. TIMERS A AND B RESTART
S15
REQUEST IS RECEIVED ?
YES
S19
REPLY TO REQUEST IS SENT TO DCD. TIMERS A AND B RESTART.
NO
S16
I/P SETTING DATA IS RECEIVED ?
YES
S20
I/P DATA IS SET AND SENT TO DCD. TIMERS A AND B RESTART
NO
S17
YES
EOT IS RECEIVED ?
NO
S18
TIMER A IS TIMEOUT (> Ta)?
NO
YES
B

FILED VIA EFS-WEB

FIG.9



OBLON, SPIVAK, ET AL
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INV: Masaaki OGURA
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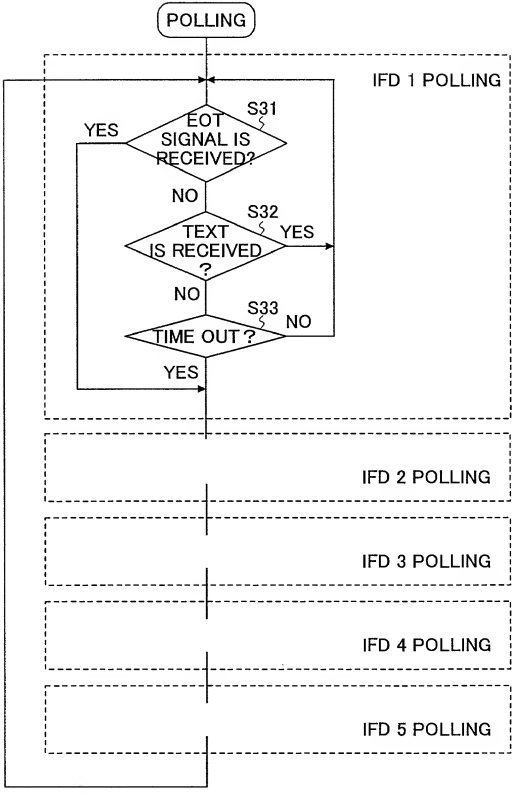
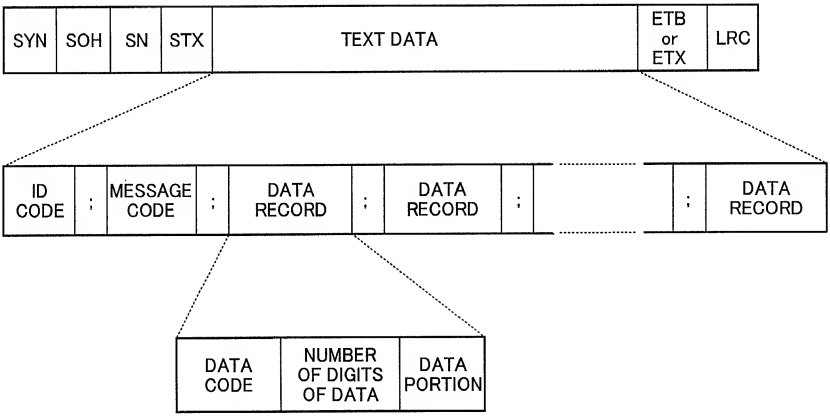
FIG.10


POLLING
IFD 1 POLLING
S31
EOT SIGNAL IS RECEIVED?
YES
NO
S32
TEXT IS RECEIVED ?
YES
NO
S33
TIME OUT ?
NO
YES
IFD 2 POLLING
IFD 3 POLLING
IFD 4 POLLING
IFD 5 POLLING

FOREIGN CORRESPONDENCE

FIG.11



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INV.: Makiko OGURA
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INV.: Masaaki OGURA
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FIG.12

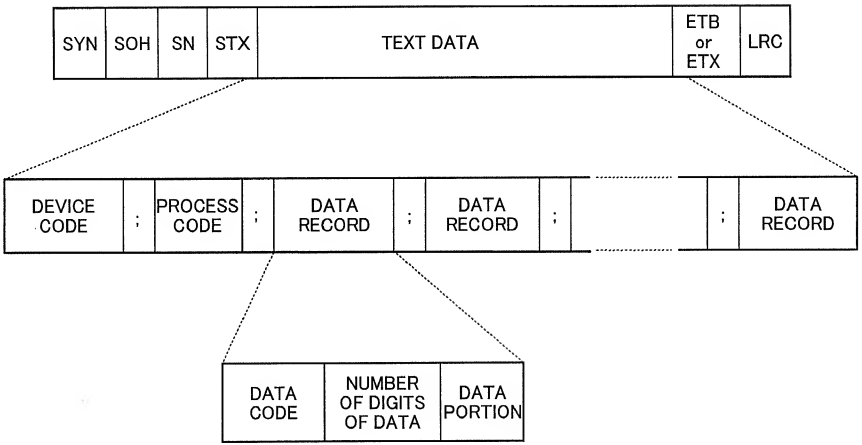

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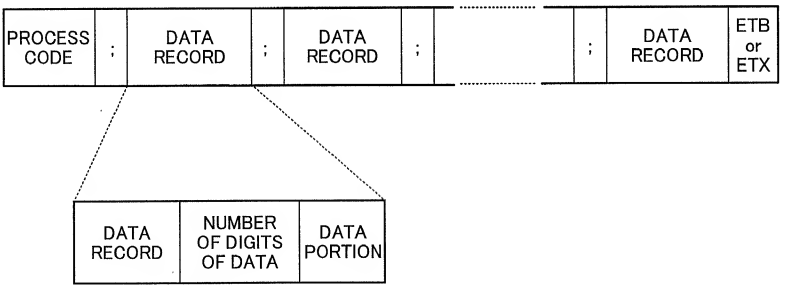
FIG.13



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INV.: Masaaki OGURA
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FIG.14A

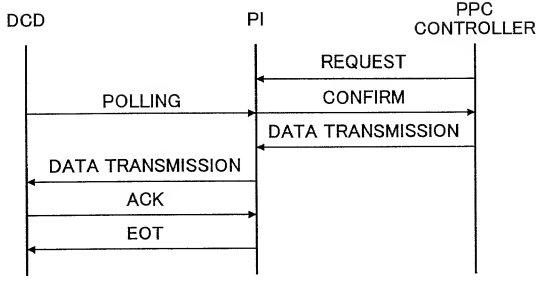

FIG.14B

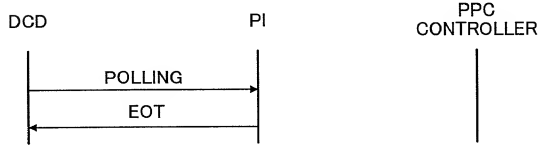

FIG.14C

SYN	1	*	ENQ

SYN: 16H
1 : 31H
* : DEVICE CODE(0～V)
ENQ: 05H

FIG.15A

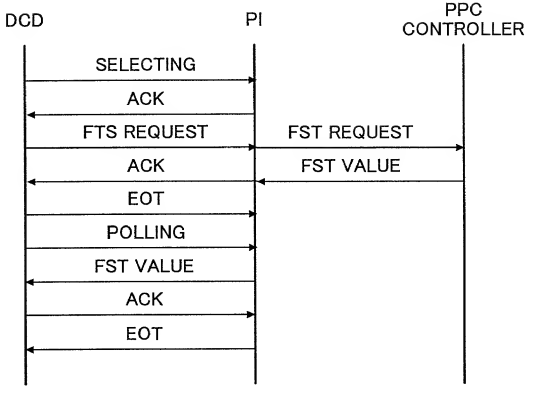

FIG.15B

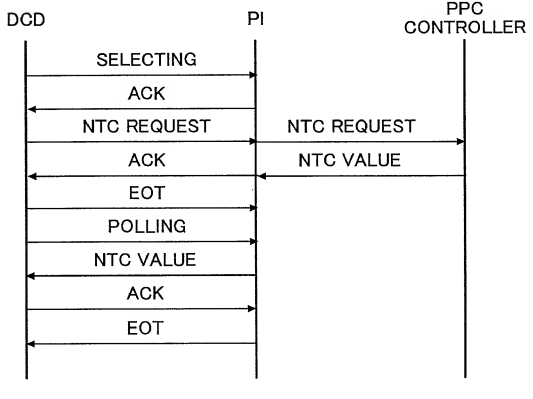

FIG.16A

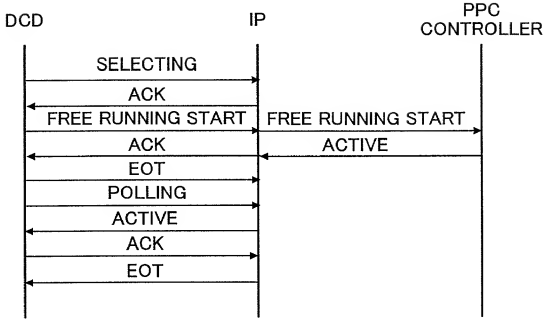

FIG.16B

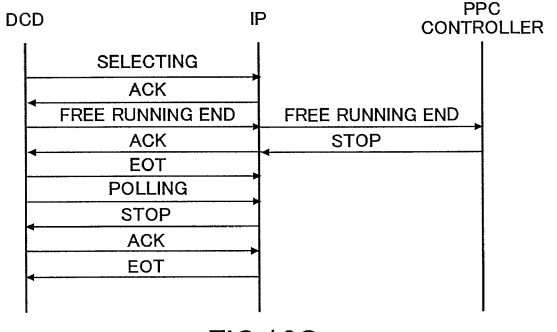

FIG.16C

SYN	2	*	ENQ

SYN: 16H
2 : 32H
* : DEVICE CODE(0～V)
ENQ: 05H

FIG.17

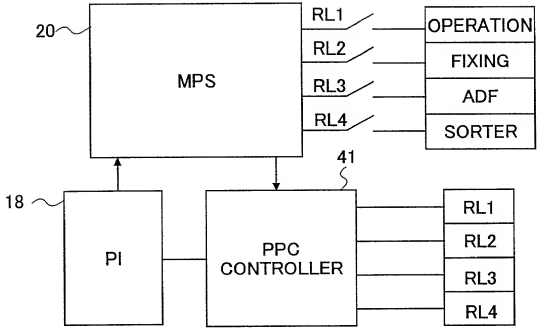
20
MPS
RL1
RL2
RL3
RL4
OPERATION
FIXING
ADF
SORTER
41
18
PI
PPC CONTROLLER
RL1
RL2
RL3
RL4

FIG.18

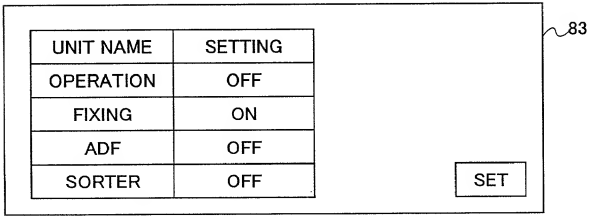
83
UNIT NAME
SETTING
OPERATION
OFF
FIXING
ON
ADF
OFF
SORTER
OFF
SET

FIG.19

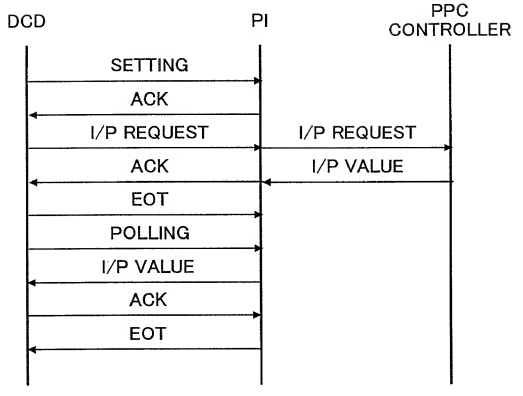

FIG.20

SYN	SOH	SN	STX	P/S PORTION SELECTION INFORMATION	I/P REQUEST INFORMATION (FTS REQUEST)	ETX	LRC

FIG.21A

BIT	P/S PORTION SELECTION INFORMATION	REMARKS
0	OPERATION 1;ON,0;OFF	
1	FIXING 1;ON,0;OFF	
2	ADF 1;ON,0;OFF	
3	SORTER 1;ON,0;OFF	
4		
5		
6		
7		

FIG.21B

I/P REQUEST INFORMATION (FTS REQUEST)	REMARKS
5101105020000	

FIG.22

SYN	SOH	SN	STX	P/S PORTION SELECTION INFORMATION	ETX	LRC